

?

H

I G H

●

•

SCT1271

G K HDC  H DGN

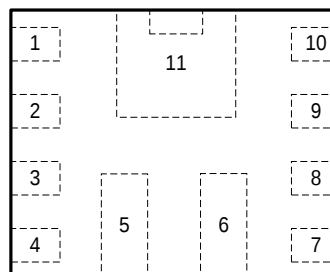
K DG G C DGB I DC

ORDERABLE DEVICE	PACKAGING TYPE	STANDARD PACK QTY	PACKAGE MARKING	PINS	PACKAGE DESCRIPTION

HDA I B MB B G I C H

DESCRIPTION	MIN	MAX	UNIT
			o
			o
			o

EC DC G I DC



EC C I DCH

NAME	NO.	PIN FUNCTION

G DBB C DE G I C DC I DCH

PARAMETER	DEFINITION	MIN	MAX	UNIT

H G I C H

PARAMETER	DEFINITION	MIN	MAX	UNIT

I ? GB A C DGB I DC

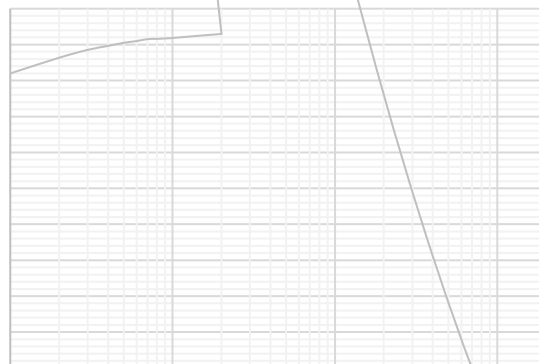
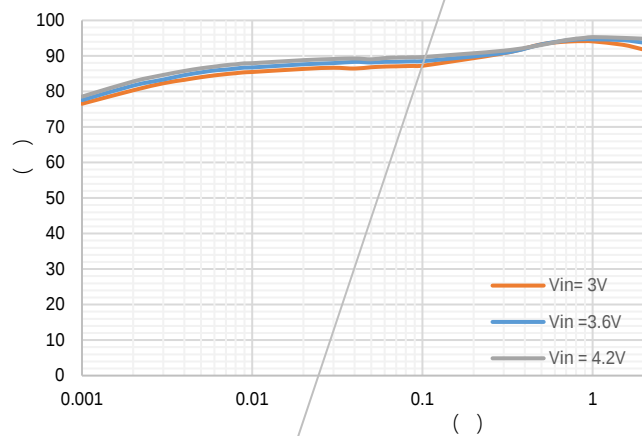
PARAMETER	THERMAL METRIC	QFN-11L	UNIT

SCT1271

A I G A ? G I G H H

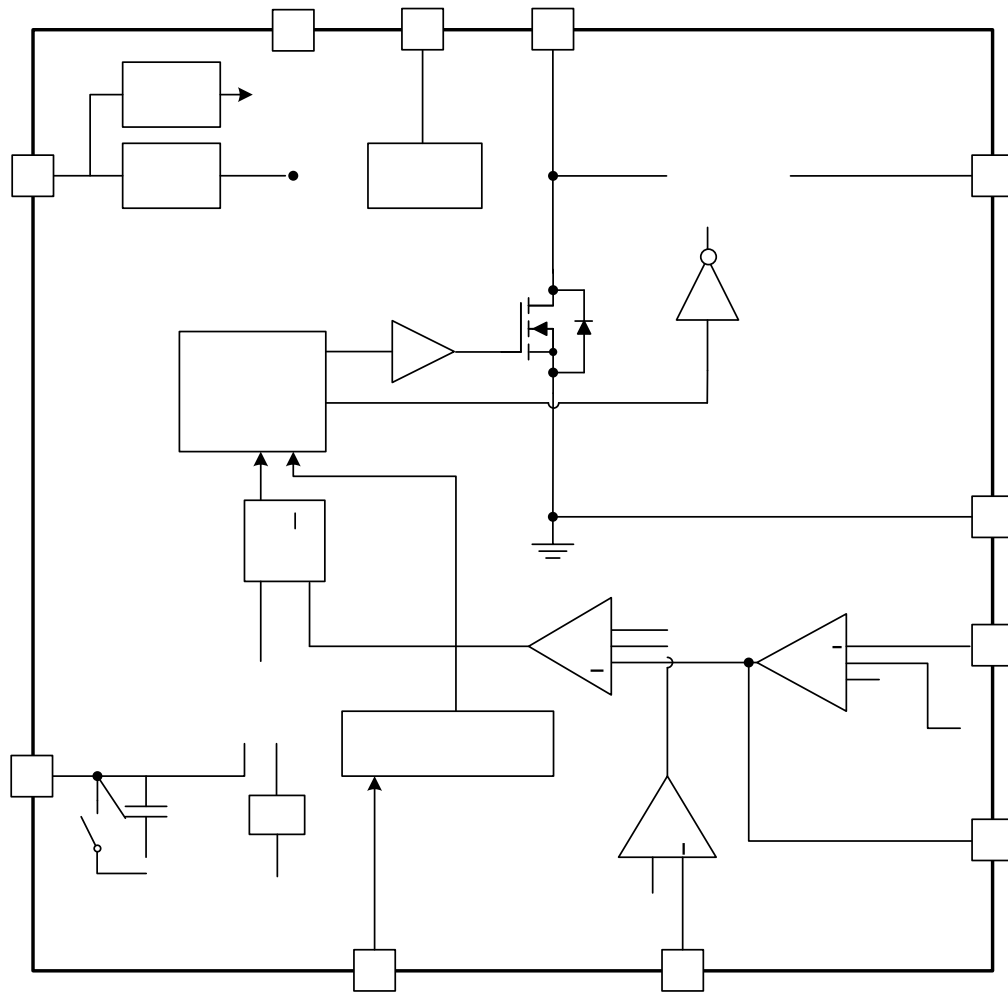
SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Power Supply and Output						
Reference and Control Loop						
Power MOSFETs						
Current Limit						
Enable						
Switching Frequency						
Protection						

INE A 2 G I G H H



C I DC A AD

G B



DE G I DC

Overview

Adjustable Peak Current Limit

-
-

Adjustable Switching Frequency

-
-
-
-
-

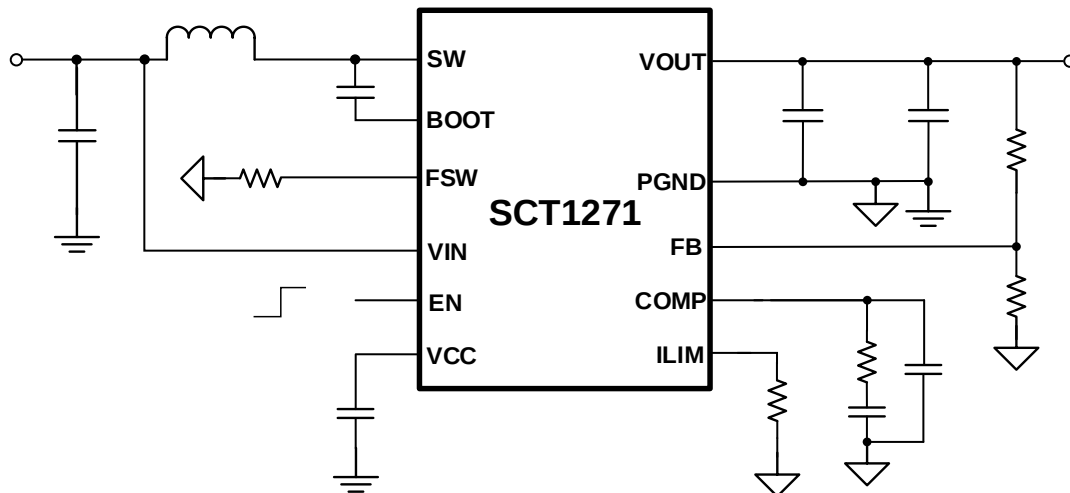
Over Voltage Protection and Minimum On-time

Thermal Shutdown

SCT1271

EEA I DC C DGB I DC

Typical Application



Design Parameters

Design Parameters	Example Value

Switching Frequency

-
-
-
-
-

F _{SW}	R _{FREQ}

Peak Current Limit

-
-

I _{LIM}	R _{LIM}

Output Voltage

-

V _{OUT}	R ₁	R ₂

Inductor Selection

Table 4. Recommended Inductors

Part Number	L (uH)	DCR Max (mΩ)	Saturation Current/Heat Rating Current (A)	Size Max (LxWxH mm)	Vendor

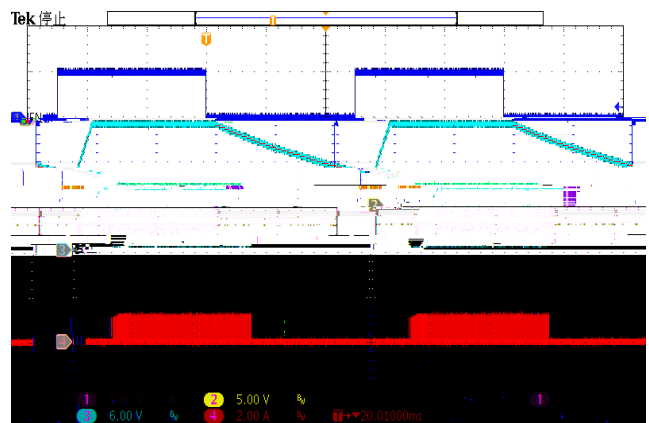
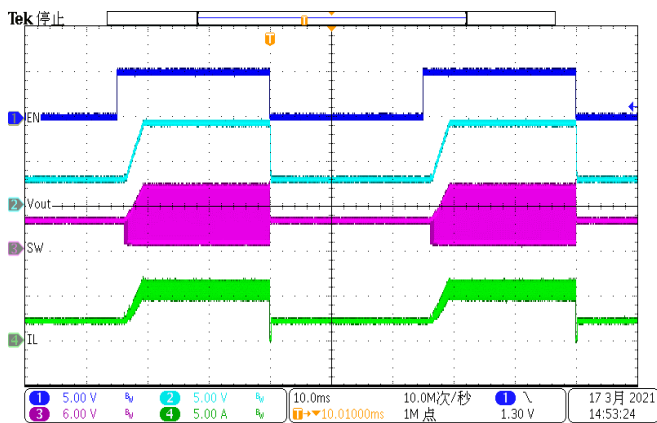
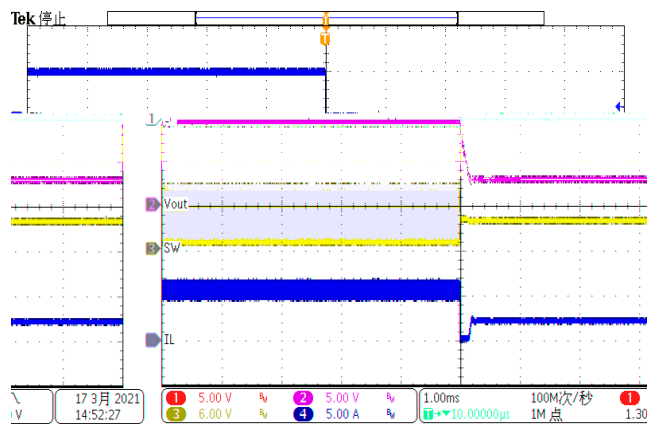
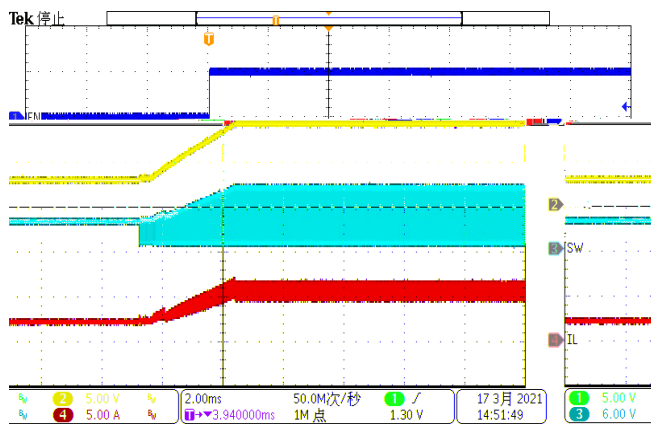
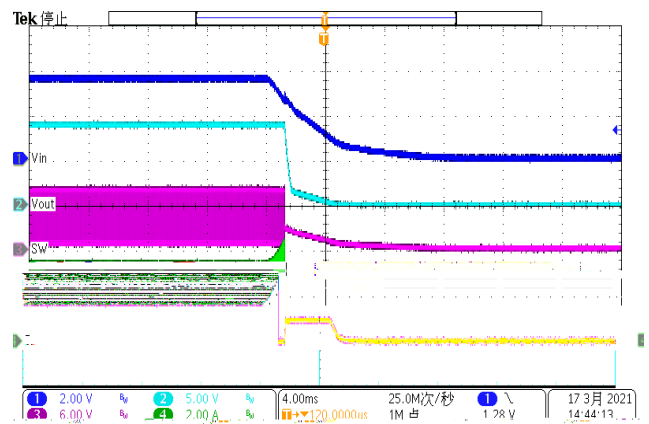
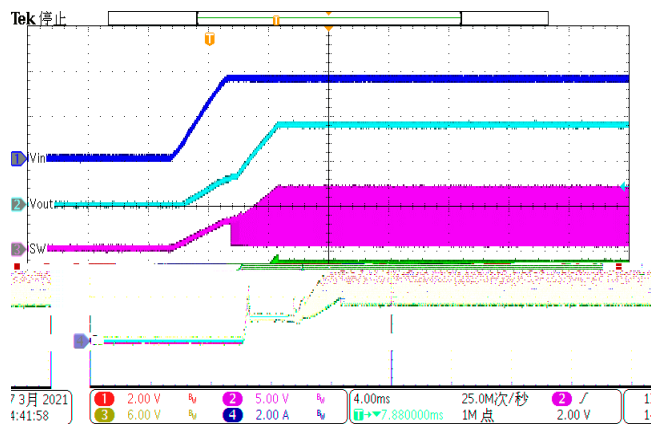
Input Capacitor Selection

Output Capacitor Selection

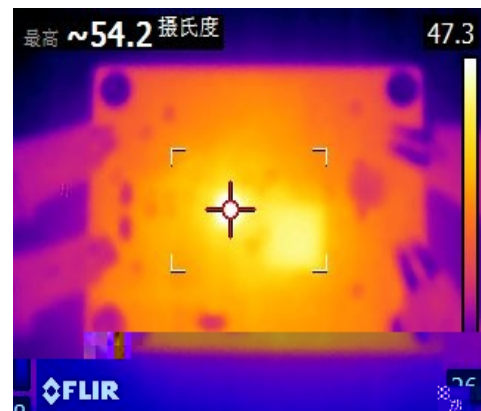
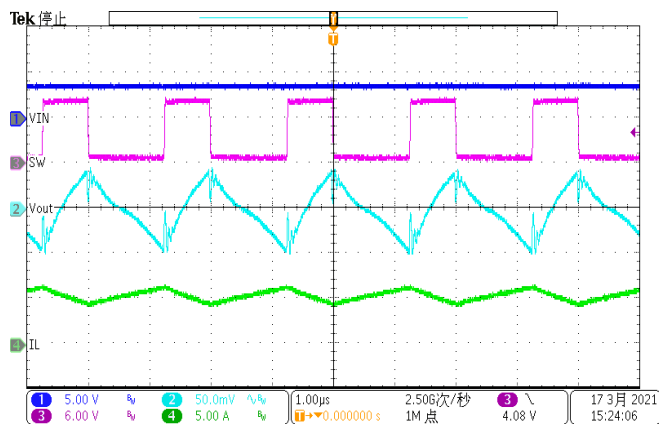
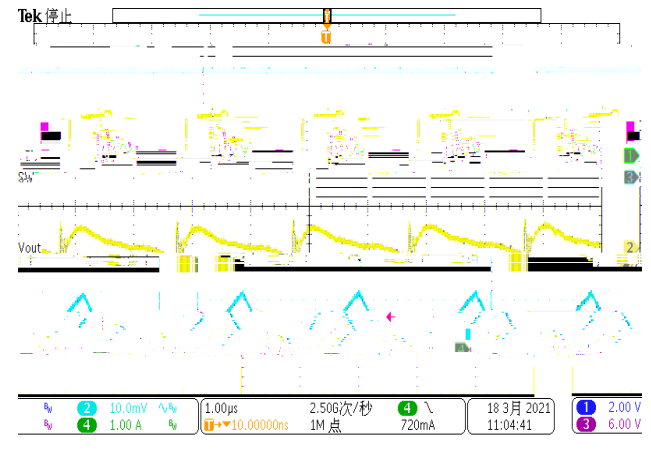
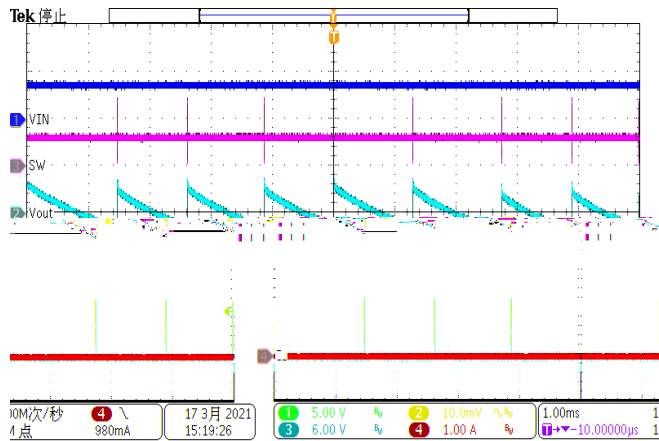
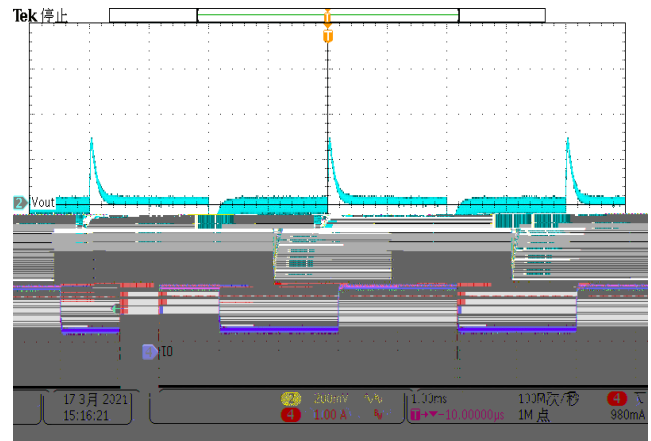
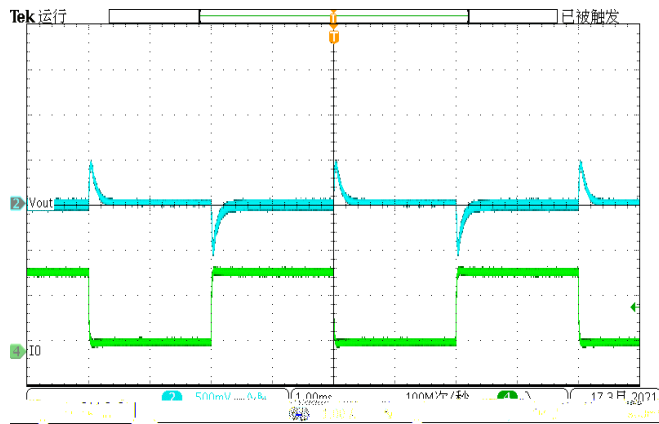
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Loop Stability

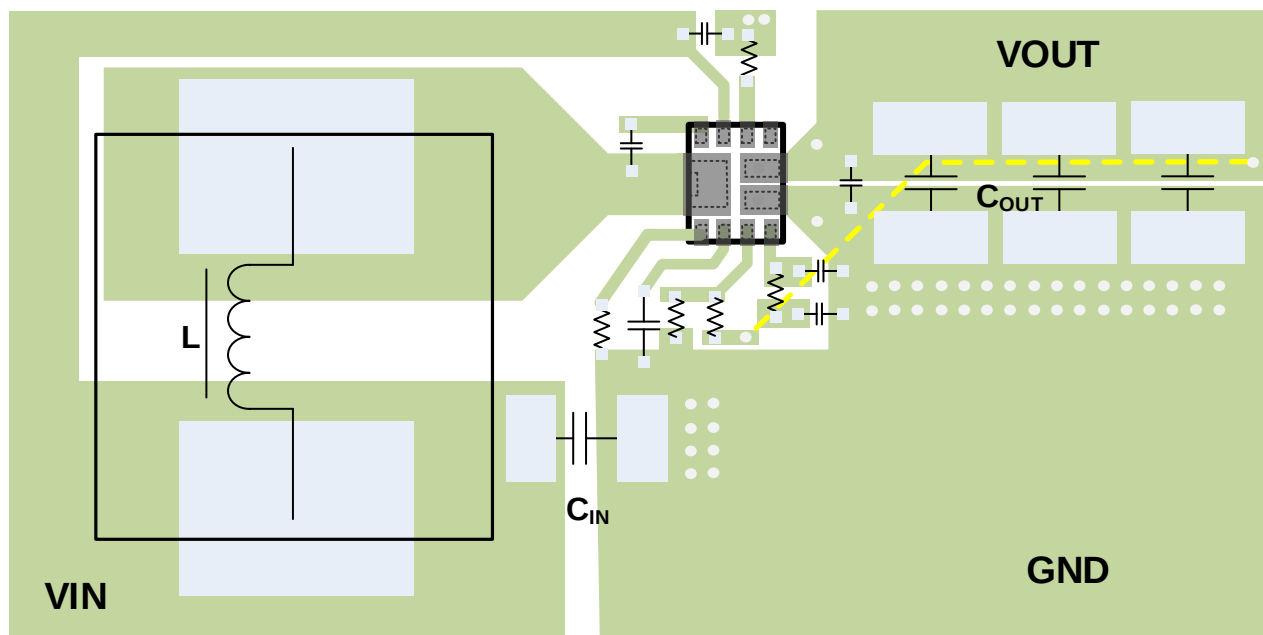
Application Waveforms



Application Waveforms(continued)



Layout Guideline



Thermal Considerations

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I E C G A C DGB I DC

