

3.8V-30V Vin, 3A, High Efficiency Synchronous Step-down DCDC Converter

FEATURES

~~(b) (7)(F), (b) (7)(G)~~ 08-06-2019 15:00 Q EM /Spa

2425 ETQ EM /Span

REVISION HISTORY

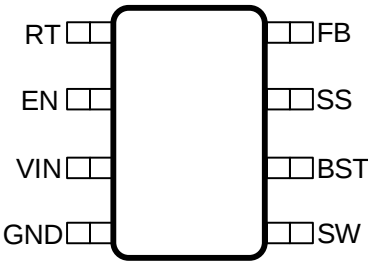
DEVICE ORDER INFORMATION

MPBCP C BCTAC	N AI EE RWNC	QR LB PB N AI ORW	N AI EC K PI EE	NGQ	N AI EC BCQAPORIL	KQ

ABSOLUTE MAXIMUM RATINGS

BCQAPORIL K K V SL

PIN CONFIGURATION



Dsp /

RECOMMENDED OPERATING CONDITIONS

N P K CRCP	BCDGLML	KG	K V	SLR

ESD RATINGS

N P K CRCP	BCDGLML	KG	K V	SLR

(1)
(2)

THERMAL INFORMATION

	K CRP A	DAQMR361	SLR
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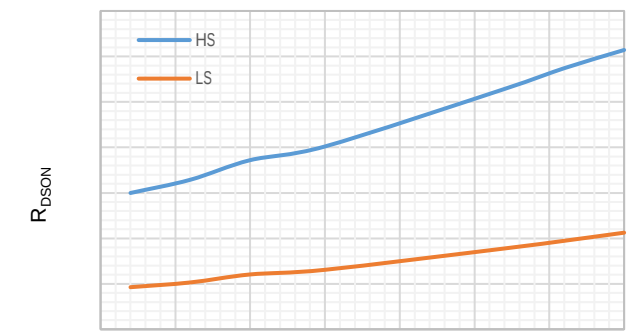
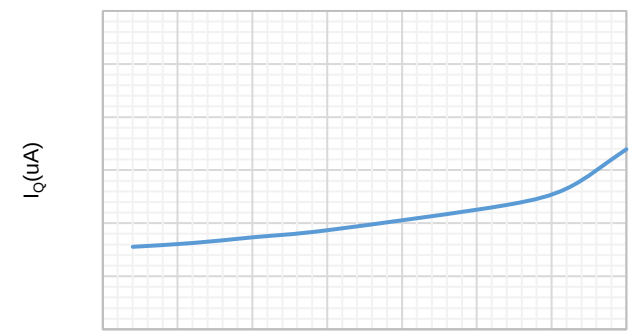
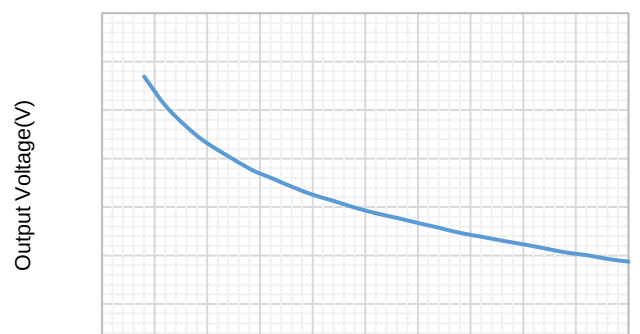
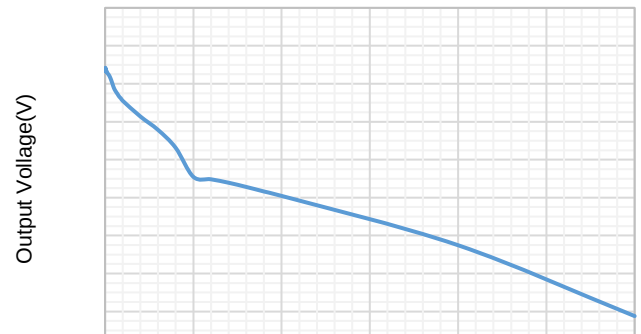
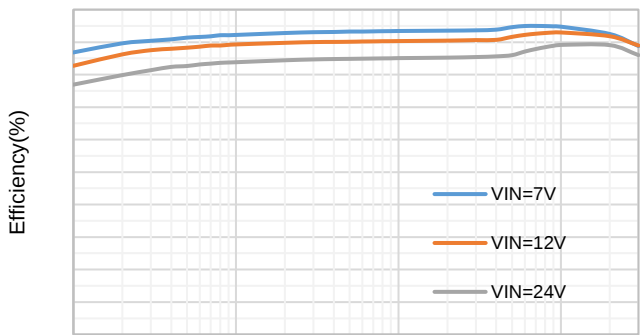
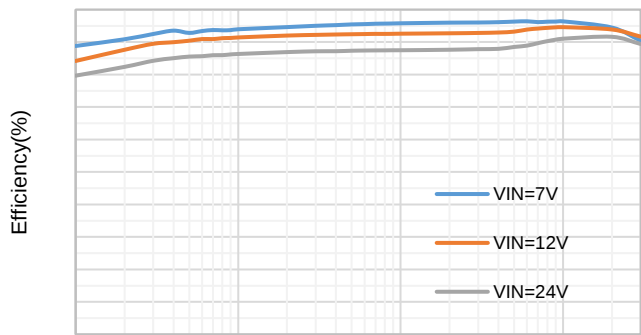
ELECTRICAL CHARACTERISTICS

QWK M	N P KCRCP	RCQR AMLBQML	KG	RWN	K V	SLQ
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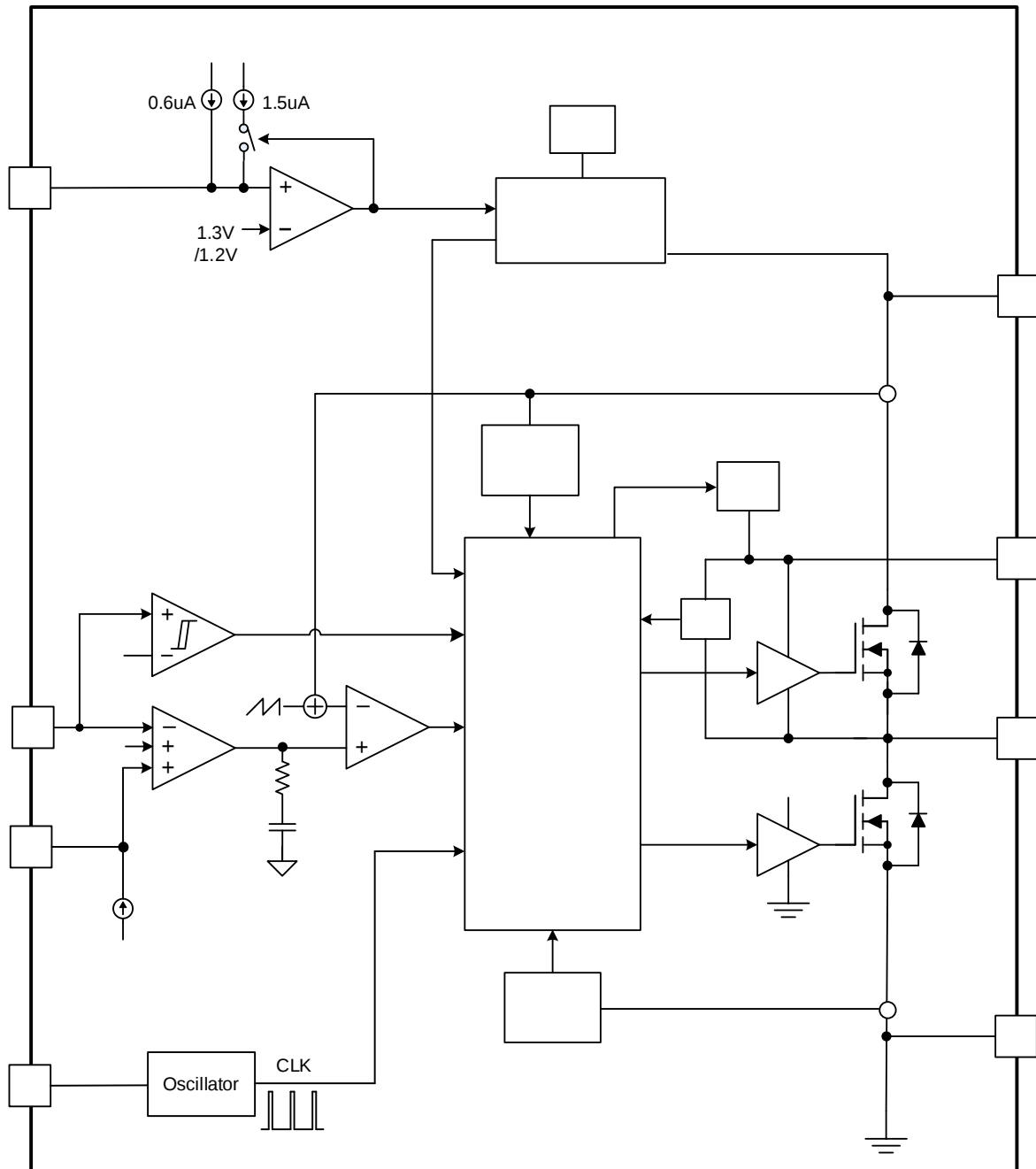
Nmu pQsnn w

QWK M	N P KCRCP	RCQR AML B&ML	K& RWN K V	SL&

TYPICAL CHARACTERISTICS



FUNCTIONAL BLOCK DIAGRAM







Qu gaf g e Dp os l aw

nu Bpm msr Dsl argn

Msrsnr Tmr e

b " (b e e ed f eb
ee f

T _{MSR}	P _i	P ₀

Qu gaf g e Dp os l aw

G sampQ argn

6 Q F U H D V H

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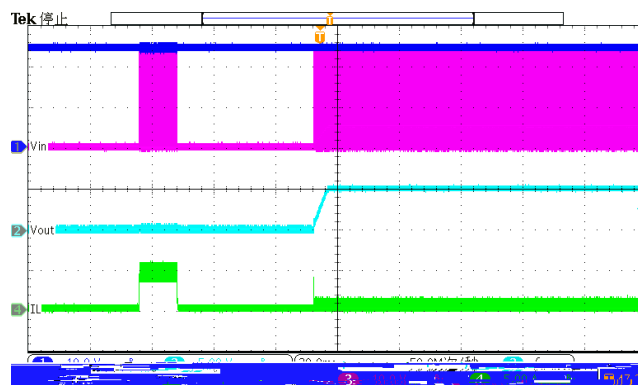
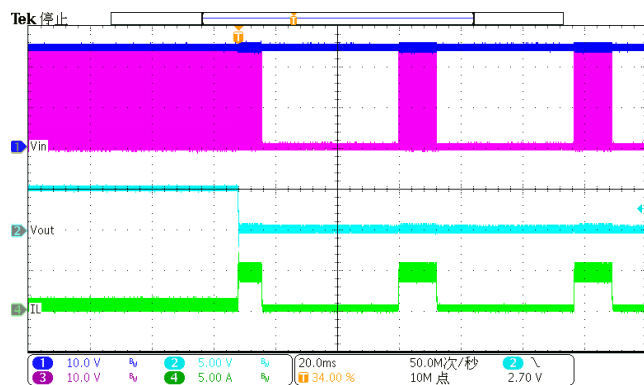
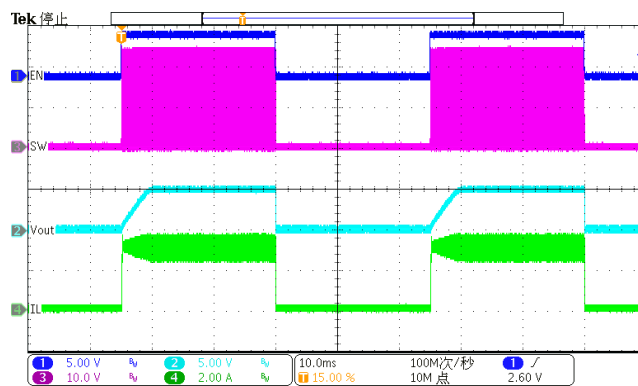
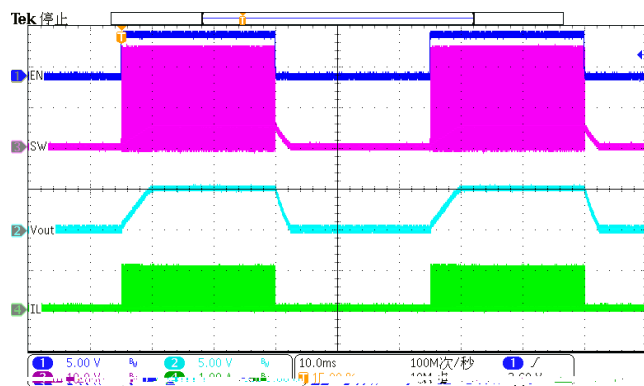
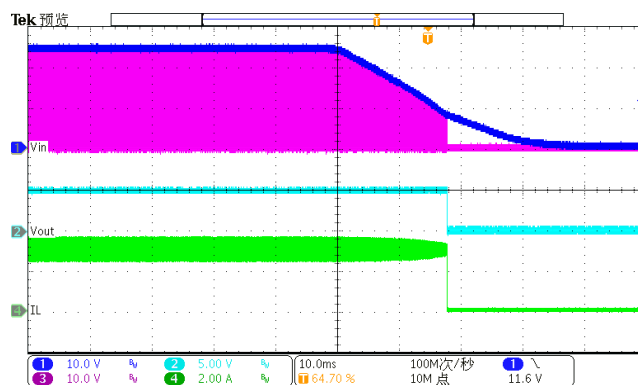
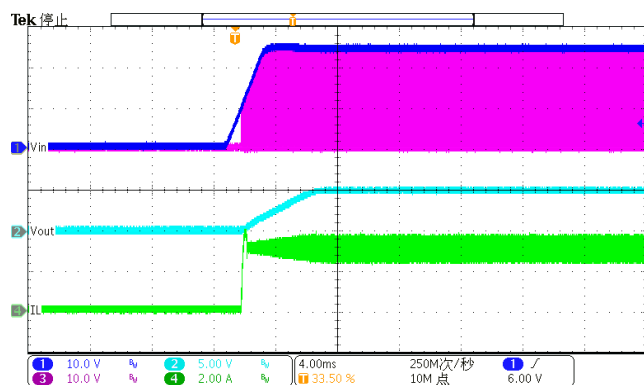
mmrqrp n A n agmpQ argnl

Msrnsr A n agmpQ argnl

R 08Rwng Cvr pl Ank nml l rT s q

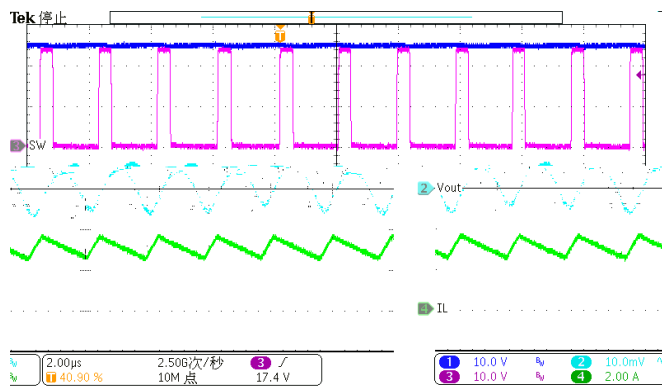
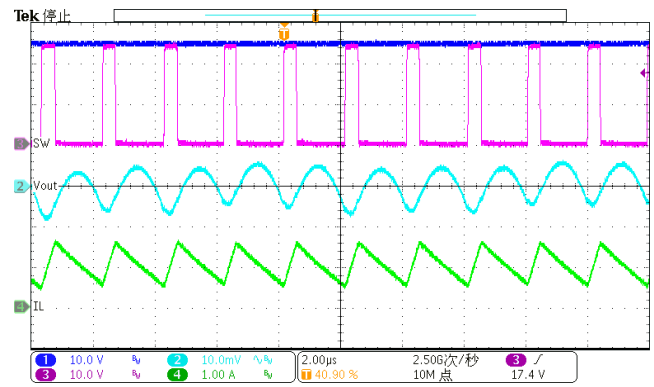
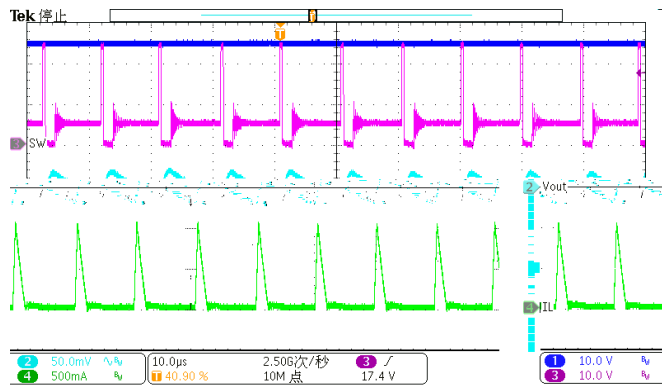
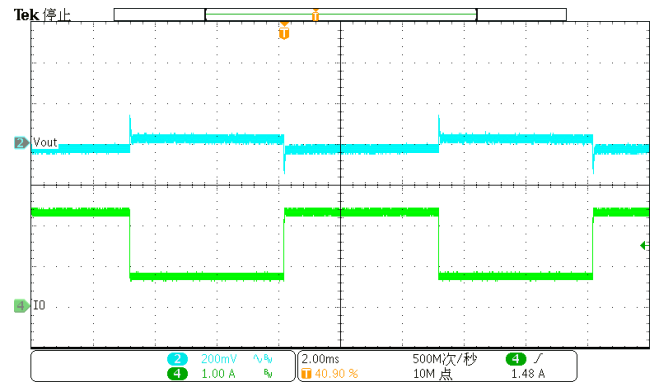
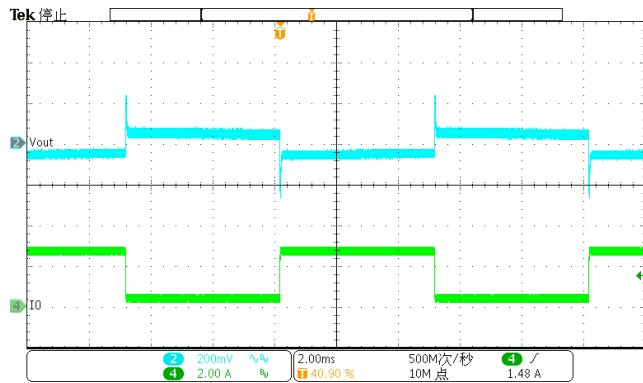
VOUT	FREQUENCY	R1	R2	R _{RT}	L1	C8	COU _T
5V	500kHz	53.6 kΩ	10.2 kΩ	Floating	6.8 uH	Optional	44uF
12V	500kHz	143 kΩ	10.2 kΩ	Floating	15 uH	Optional	44uF
5V	1.15MHz	53.6 kΩ	10.2 kΩ	GND	3.3 uH	Optional	33uF
12V	1.15MHz	143 kΩ	10.2 kΩ	GND	6.8 uH	Optional	33uF

nn g rgrl U t dmpk q



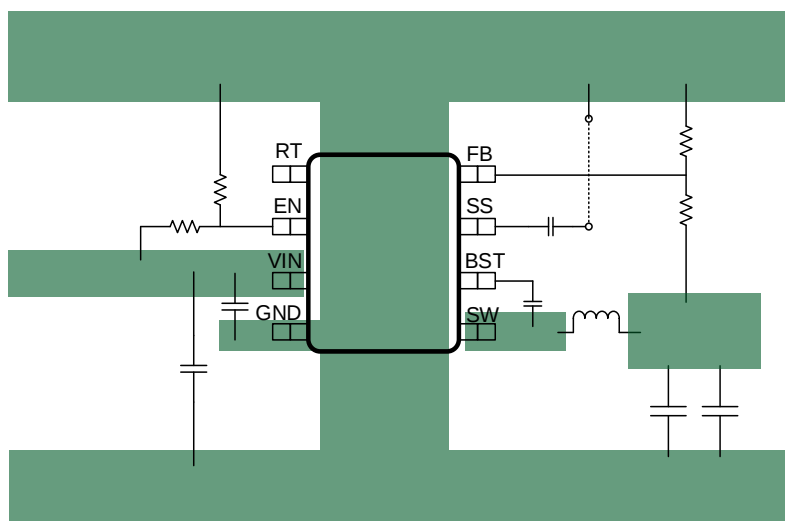
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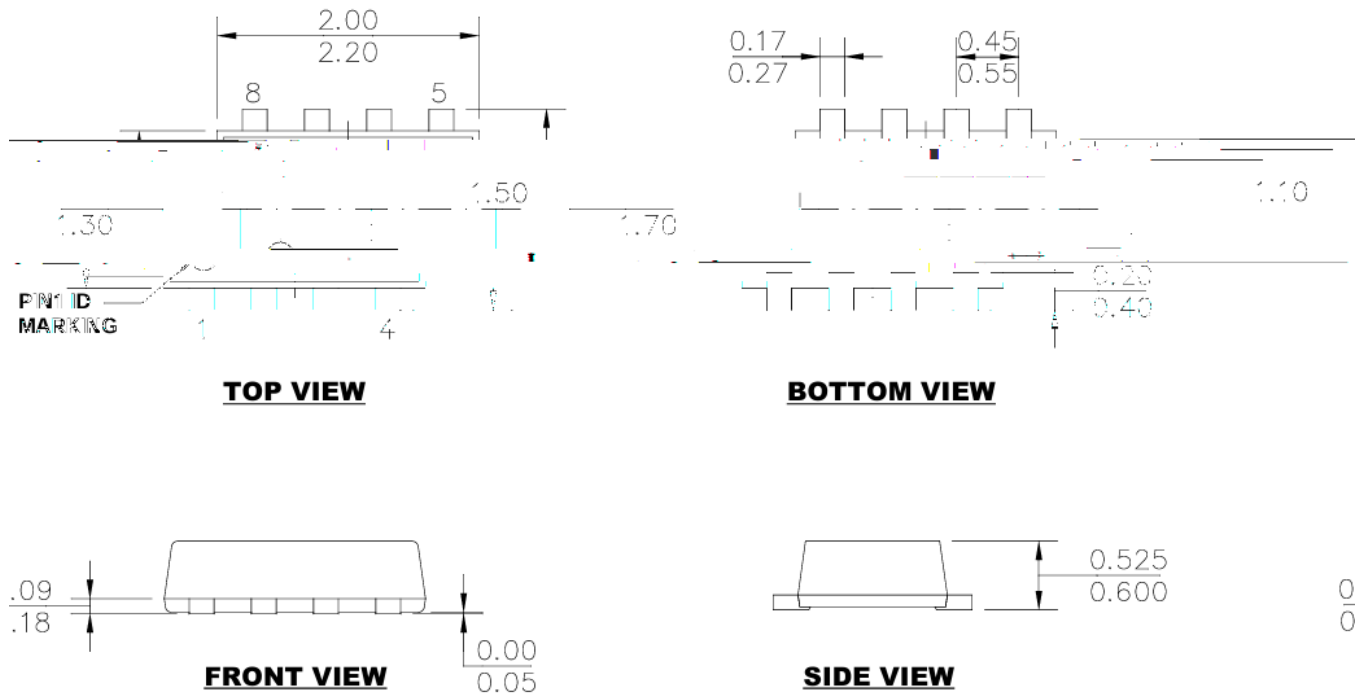


WMSR E sg g

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- 3.
- 4.
- 5.
- 6.
- 7.
- 8.



PACKAGE INFORMATION



NOTE:

1) ALL DIMENSIONS ARE IN MILLIMETERS.

2) PACKAGE LENGTH DOES NOT INCLUDE

JEONOR-GATE FLAT.

3) PACKAGE WIDTH DOES NOT INCLUDE

PROTRUSION.

4) PACKAGE LENGTH DOES NOT INCLUDE

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RECOMMENDED LAND PATTERN

